

Speakers Line-Up

5:25 – 5:45pm

Silicon Photonics,
Co-Package Optics
and the future of
heterogeneous integration



Yuval Shay
Director
SYNOPSYS

5:45 – 6:05pm

The co-existence of
monolithic and hybrid
integration for silicon
photonics
– A Foundry View



Vikas Gupta
Senior Director
GLOBALFOUNDRIES

6:05 – 6:25pm

Use cases for hybrid
integration in high volume
silicon photonics
applications



Robert Blum
Head of SiPh Strategy
INTEL FOUNDRY SERVICES

6:25- 6:45pm

Photonic Integrated
Circuits for High Density
Co-Packaged Optics



Vivek Raghuraman
Director
BROADCOM

6:45 – 7:05pm

Co-packaged Photonics for
next-gen AI/ML systems



Ashkan Seyedi
Product Architect
NVIDIA